



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov

BIBDATASHEET**CONFIRMATION NO. 6844**

Bib Data Sheet

SERIAL NUMBER 10/748,090	FILING DATE 12/29/2003 RULE	CLASS 438	GROUP ART UNIT 2825	ATTORNEY DOCKET NO. P16015C
-----------------------------	---------------------------------------	--------------	------------------------	-----------------------------------

APPLICANTS

Justin K. Brask, Portland, OR;

Mark L. Doczy, Beaverton, OR;

John P. Barnak, Portland, OR; Robert S. Chau, Beaverton, OR;

 ** CONTINUING DATA *yes V. 4* *****

This application is a CON of 10/391,816 03/18/2003 PAT 6,696,327

 ** FOREIGN APPLICATIONS *NONE V. 4.* *****

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 04/12/2004

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged Examiner's Signature <i>V. P. S. M. V. 4</i> Initials <i>V. 4</i>	OR	1	1(2)	1(2)

ADDRESS

Michael A. Bernadicou
 BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP
 Seventh Floor
 12400 Wilshire Boulevard
 Los Angeles, CA
 90025

TITLE

Method for making a semiconductor device having a high-k gate dielectric

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT	<input type="checkbox"/> All Fees
		<input type="checkbox"/> 1.16 Fees (Filing)
		<input type="checkbox"/> 1.17 Fees (Processing Ext. of time)